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Addressing Diversity in Data Center Networks.

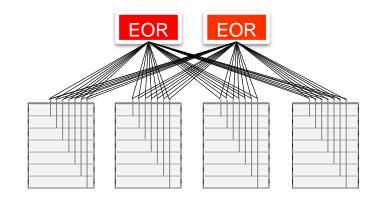
Yaniv Kopelman, Networking CTO, Marvell Semiconductor

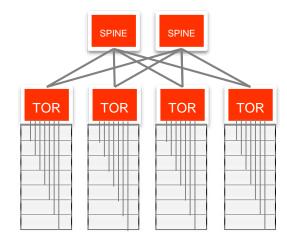


Mega Scale Data Center **EOR EOR** SPINE **SPINE** TOR TOR TOR TOR **SPINE** SPEED **TOR TOR TOR**

Mega Scale Data Center

- Mega Scale DC Requirements:
 - Very high throughput
 - Compute: 50G -> 100G -> 200G
 - Network: 100G -> 400G -> 800G
 - Storage and Compute Convergence
 - Analytics
 - High radix
 - Large forwarding tables
 - One-Size-Fits-All

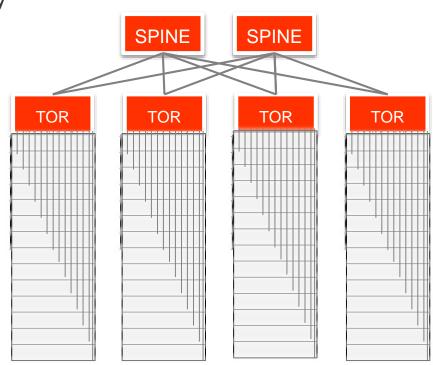




Enterprise and Private Cloud



- Enterprise DC and Private DC, typically try to duplicate MSDC architecture
- Enterprise and Private DC Requirements:
 - Lower throughput:
 - Compute: 1G -> 10G -> 25G
 - Network: 10G -> 40G -> 100G
 - Higher density racks
 - Feature rich



Edge and uEdge Cloud

- Edge DC DC closer to the premises
- uEdge DC DC in a Rack or DC in a Box
- Edge DC Requirement
 - Real Time response for mission critical tasks
 - Security
 - Roaming
 - Low Power
 - Smaller form factor



DC Hubs - Colocation

- Exchange hubs for Cloud Service Providers
- Colocation DC Requirements:
 - Security
 - Diverse I/O configuration
 - Automatic Reconfiguration
 - Seamless migration from one CSP to another
 - Resiliency for disaster recovery
 - SLA's different QoS and service levels



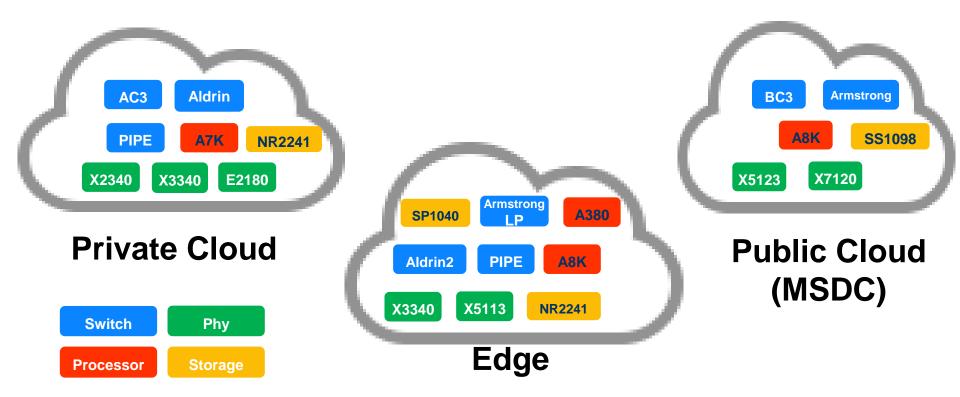
| Mega Scale DC | Private Cloud | Edge Cloud | Colocation |
|---------------------------------|----------------------|---|--|
| | (C) | | |
| Higher throughput | Lower throughput | Low Power | Diverse I/O Configuration |
| Storage and Compute Convergence | Higher density racks | Real Time response for mission critical tasks | Seamless migration from One CSP to another |
| Analytics | Feature rich | Security | Resiliency for disaster recovery |
| High Radix | | Roaming | Automatic reconfiguration |
| Large forwarding tables | | Smaller form Factor | |
| One Size First All | | | |



Chips for Networking and Storage Infrastructure

Embedded Processor Switch Storage PHY

Full Portfolio of Products



Introducing Modularity



Marvell Prestera Switch – Modular Architecture

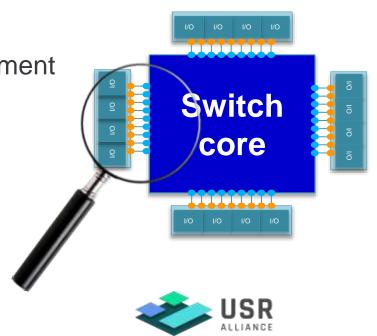
Enables different I/O configuration

Different I/O speeds

Optimized configuration per market segment

Fully featured packet processor

- Addressing Diversity
 - Diverse Bandwidth solutions
 - Diverse I/O portfolio
 - Rich Features set: e.g. Roaming, Security
 - Enhanced Analytics
 - Low Latency
 - Advanced QoS



Opening the Door for Chip Interconnect

Open Hardware

Open Software

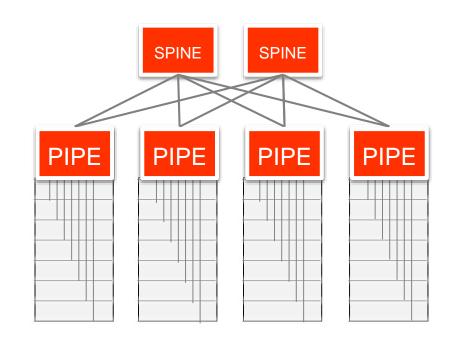
Open System Firmware

Open Chip Interconnect



System Modularity – Prestera Port Extenders

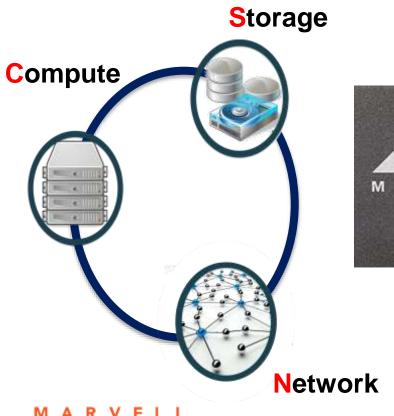
- PIPE Passive Intelligent Port Extender
- Replacing TOR switches with Passive Port Extenders
- Simpler management
- Plug and Play between vendors using standard 802.1BR protocol
- Addressing Diversity
 - Smaller network scale
 - Lower Power
 - Lower TCO



30% of the RBOM

40% of the Power – Fan less design

PHY Solutions



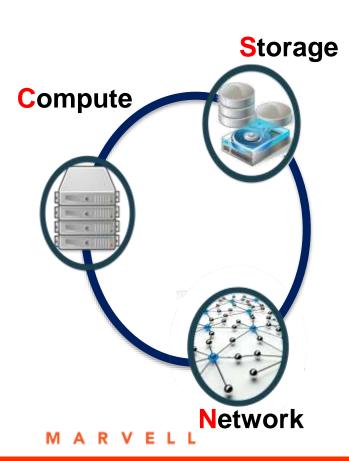


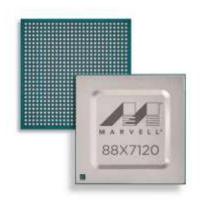
Cable Plant: Data Center, Carrier, Enterprise



1GbE, 2.5/5GbE, 10GbE, 25GbE, 50GbE, 100GbE, 200GbE, 400GbE

PHY Solutions





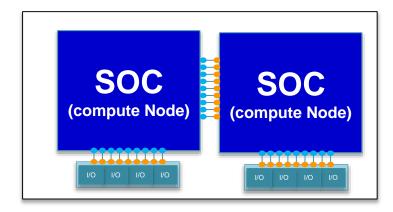


- 16 Port 50GbE PHY
- Dual 400GbE Retimer
- Quad 100GbE Reverse Gearbox
- Fully compliant to IEEE 802.3cd and 802.3bs standards
- Optimized for QSFP-DD and OSFP applications

ARM Based Modular Compute Nodes

- QUAD CORE ARM A72
 - 2 Clusters of 2xCA72
 - High performance Coherent interconnect
 - Virtualization support (Core and IO)
 - Secured boot support

- ARM Based Embedded processor for TOR and SPINE
 - SONIC and ONIE ported to ARM







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Address Future Trends

Artificial Intelligence driving even higher Bandwidth

 Convergence of Storage Compute and Networking

 Larger Networks -> drive larger scales of switches

Optical Interfaces directly from the Server



THE REASON TO WORK WITH MARVELL

Marvell is the ONE company that can deliver a complete portfolio of Networking and Storage Infrastructure for all Data Centers



Experience

Experienced veteran networking team
Execution track record



End to End Product Portfolio

Multiple generations of Eth Switch, embedded processors and Phy products
Complementary products (e.g. Storage) from Marvell rich



Innovation

portfolio

Differentiating, feature rich, highest capacity DC switch Modular architecture and scalable design The information contained in this presentation is provided for informational purposes only. While efforts were made to verify the completeness and accuracy of the information contained in this presentation, it is provided "AS IS", without warranty of any kind, express or implied. This information is based on Marvell's current product roadmap, which are subject to change by Marvell without notice. Marvell assumes no obligation to update or otherwise correct or revise this information. Marvell shall not be responsible for any direct, indirect, special, consequential or other damages arising out of the use of, or otherwise related to, this presentation or any other documentation even if Marvell is expressly advised of the possibility of such damages. Marvell makes no representations or warranties with respect to the contents of the presentation and assumes no responsibility for any inaccuracies, errors or omissions that may appear in this presentation.



